



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/879,231
Filing Date June 11, 2001
Inventor Garo J. Derderian, et al
Assignee Micron Technology, Inc.
Group Art Unit 2818
Examiner T. Le
Attorney's Docket No. MI22-1748
Title: Capacitor Fabrication Methods and Capacitor Constructions

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

To: Assistant Commissioner for Patents
Washington, D.C. 20231

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This Supplemental Information Disclosure Statement is being filed in an abundance of caution to ensure consideration of the references listed on the attached form PTO-1449.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 09,653,149, filed August 31, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 16 Dec 2002

By: _____

James E. Lake
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